

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
)	
Takashi OKUDA et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: November 19, 2003)	Confirmation No.: Unassigned
)	
For: MODULATOR PROVIDING ONLY)	
QUANTIZATION ERROR)	
COMPONENT TO $\Delta\Sigma$)	
MODULATOR)	

FIRST
INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the cited documents is enclosed.

Japanese Patent Nos. 3-22626, 6-53836, 11-308110, and 2002-76902 are discussed in the specification.

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since the documents are being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOAME, SWECKER & MATHIS, L.L.P.

Date: November 19, 2003

By: 

Platon N. Mandros
Registration No. 22,124

P.O. Box 1404
Alexandria, Virginia 22313-1404
(703) 836-6620

ATTORNEY'S DKT No.
009683-486

APPLICATION No.
Unassigned

APPLICANT
Takashi OKUDA et al.

FILING DATE
November 19, 2003

GROUP
Unassigned

**Examiner
Initials**

Document
NumberKind Code
(if known)Name of Patentee or Applicant
of Cited Document

Issue/Publication
Date
(MM-DD-YYYY)

**Examiner
Initials**

Document
NumberKind Code
(if known)

Country

Date of Publication
(MM-DD-YYYY)

Translation	
Yes	No

*3-22626

Japan

01-31-1991

Abst

***6-53836**

Japan

02-25-1994

Abst

*11-308110

Japan

11-05-1999

Abst

*2002-76902

03-15-2002

Abst

**Examiner
Initials**

Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

Gabor C. TEMES et al., "Architectures for $\Delta\Sigma$ DACs," Delta-Sigma Data Converters, IEEE Press, 1997, pp. 309-313.

David JOHNS et al., "Analog Integrated Circuit Design," John Wiley & Sons Inc., 1997, pp. 547-551, 568-571.

**Examiner
Signature**

Date
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.

***Also discussed in the specification**